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	Application Number		10613403	
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INFORMATION DISCLOSURE	First Named Inventor	Jim N	EWTON	
STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Art Unit		2825	
(Not for Submission under 57 Of K 1.55)	Examiner Name	Kik, P	hallaka	
	Attorney Docket Number	er	CA7020002001	

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	1	Cadence "Cell Design Tutorial" Title Page, Table of Contents, Chapters 5 and 6, June 2000, Cadence Design Systems, Inc., San Jose, CA.							
	2	Cadence "Virtuoso Parameterized Cell Reference" 1999, 35 pgs., Cadence Design Systems, Inc., San Jose, CA.							
	3	BARNES, T.J. "SKILL: A CAD System Extension Language" Proceedings of the 27th ACM/IEEE Design Automation Conference, Orlando, FL, June 24-28, 1990, pp. 266-271.							
	4	JINGNAN, X. et al. "A SKILL-based Library for Retargetable Embedded Analog Cores" Proceedings of the Conference on Design, Automation and Test in Europe, Munich, Germany, 2001, pp. 768-769.							
	5	ZAMAN, M.H. et al. "An Environment for Design and Modeling of Electro-Mechanical Micro-Systems" Journal of Modeling and Simulation of Microsystems, 1999, Vol. 1, No. 1, pp. 65-76, located at http://www.conventor.com/media/papers/jmsm_1999.pdf							
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